# St. JOSEPH'S COLLEGE (AUTONOMOUS) TIRUCHIRAPALLI-620 002. INDIA

#### DEPARTMENT OF ELECTRONICS

Chief Patron: Rev. Dr. Leonard Fernando, SJ

Rector, St.Joseph's College.

Patron : Rev. Dr. A. Antony Pappuraj, SJ

Secretary, St. Joseph's College.

Chairman : Rev. Dr. M. Arockiasamy Xavier, SJ

Principal, St. Joseph's College.

Convener : Dr. I. Johnson, Head

Department of Electronics, St. Joseph's College.

Co-Convener: Rev. Fr. J.John Wilson SJ

Department of Electronics,

St. Joseph's College.

Organizing: Prof. R. Charles Silvester Joe

**Secretary** Department of Electronics,

St. Joseph's College.

Joint Secretary: Prof. A. Arputharaj

Department of Electronics,

St. Joseph's College.

### ORGANISING COMMITTEE

Prof. Dr. K. Vairamani

Prof. V. Sivakamasundari

**Prof. C. JosphineArockiamary** 

Prof. J. Sheik Shabjan

Prof. E. Thirumeni

Prof. M. Aishwarya Harini

# FOR FURTHER DETAILS CONTACT

Dr. I. Johnson, Head Convener

Mob: 9442904820

Prof:R. Charles Silvester Joe, Organizing Secretary

Mob: 9942449801

E-mail: iwrae2019@gmail.com

#### REGISTRATION FEE

- PG/UG Students of Universities/ Colleges -Rs 200/-
- Academicians, Research scholars of universities / colleges- Rs 400/-
- Executives/Professionals-from Industries- Rs.800/-
- Registration fee includes the cost of working lunch, Tea with snacks.

#### **NOTE:**

All the participants will be recognized With participation certificate.

All the participants are requested to bring their Identity Cards / Bonafide certificate from the respective college

# DATES TO REMEMBER

- Last date for registration: 07-03-2019
  the Fee may be paid In favor of "The principal St.Joseph's College, trichy as Demand Draft or online payment through the link <a href="http://www.sjctni.edu/onlinepayment">http://www.sjctni.edu/onlinepayment</a>.
- The nominations along with the fee details may be communicated through Email / 9942449801

# WORKSHOP VENUE

# SAIL AUDITORIUM

St. Joseph's College (Autonomous) Tiruchirappalli - 620 002

# International workshop On Recent Advances in Electronics



Date & Time

9<sup>th</sup> MARCH 2019 at 10 AM



# Organized by

# **DEPARTMENT OF ELECTRONICS**

**School of Physical Sciences** 

# St. Joseph's College (Autonomous)

Special Heritage Status awarded by UGC Accredited at 'A' Grade (3<sup>rd</sup> Cycle) by NAAC College with Potential for Excellence by UGC Tiruchirappalli - 620 002 Tamil Nadu, India.

#### ABOUT THE COLLEGE

St. Joseph's College is an Affiliated first grade College of the Bharathidasan University. It was established in 1844 by the fathers of Society of Jesus. The college celebrated its centenary in 1944, sesquicentenary in 1995 and acquired the Five Star status, Awarded by NAAC in 2000. The college was conferred the prestigious status of college with potential for Excellence by UGC in 2004, reaccredited by NAAC with 'A+' grade in 2006, and accredited again by NAAC with 'A' grade in 2012. In the year 2015 the college was conferred with the prestigious status of special heritage institution by UGC. The college trains young men and women of quality to be leaders in all walks of life so that they may serve the people of the nation with truth, justice and love.

# MILESOTONES OF THE COLLEGE

- 1844 Founded at Nagappattinam
- 1844 Subjects: English, Economics, Mathematics & French
- 1883 Shifted to Tiruchirappalli
- 1886-Affiliated to Madras university
- 1896-Golden Jubilee year Celebrations
- 1944-CENTENARY celebrations
- 1978-Becomes An Autonomous College
- 1982-Affiliated to Bharathidasan University
- 1994-SESQUI CENTENARY celebrations
- 1999-Natinoal Accreditation with 5 star
- 2004-College with potential for excellence by UGC
- 2006-Re-accredited with A+ grade
- 2009-DBT-FIST sponsored college
- 2012- Re-accredited with A grade(3<sup>rd</sup> cycle)
- 2014 Schools of Excellence established
- 2015-Special Heritage status by UGC
- 2018-175<sup>th</sup> DODRANSBICENTINNIAL celebrations

# PROFILE OF THE DEPARTMENT

The Department of Electronics, St. Joseph's College was established in the year 1993. The Department exceptionally offers Undergraduate (UG) and Postgraduate (PG) programmes. The UG programme was started in 1993 and PG programme was started in 1997. Right from its inception, the department has a rich tradition in teaching and works primarily on imparting the students with knowledge & skills in Electronics. The research interests of the faculty members of the department involve the wide area of applied and fundamental aspects of Electronics. The department has excellent infrastructure including separate U.G and P.G laboratories and specialized labs like VLSI, IoT, DSP and Embedded system to facilitate the graduates with latest trends and to give learning, a practical dimension. The curriculum is designed to acquire expertise in embedded system and provide practical exposure on the same. The department has always been on a progressive path. The experienced and dedicated faculty members, who have a strong commitment towards providing quality education, help to produce globally, competitive and socially sensitized UG and PG graduates and to bring out quality research in the frontier areas of Electronics.

#### MILESTONES OF THE DEPARTMENT

- 1993 B.Sc Electronics.
- 1997 M.Sc Electronics.
- 2002 Bifurcation from Physics department.
- 2002 ELEX Exhibition.
- 2005- Well Equipped LAB bifurcation from Physics.
- 2008- JOSELEX State Level technical symposium.
- 2011 JOSTRONIX Consultancy Services.
- 2014 Given birth to VISCOM under UGC Grant
- 2014 Overall best Science Department in Shift-II
- 2015 Students magazine JOSTRONIX
- 2016- Best Innovative Department Special award.
- 2017 Overall best Science Department in Shift-II
- 2018 Overall best Science Department in Shift-II
- 2018 -Department Silver Jubilee Year

#### **OBJECTIVE OF THE WORKSHOP:**

This workshop is a comprehensive discussion on Power electronics, IOT, embedded system on its architectures, applications and prototypes of IoT. This workshop also highlights the future scope of research for IoT based smart grid operations and applications of other fields such as machine learning and artificial intelligence.

#### RESOURCE PERSON/SPEAKERS

Eminent subject experts from various countries will deliver lectures. The program shall be conducted in an interactive environment.

#### WHO SHOULD PARTICIPATE

Executives and working professionals from various industries. Academicians, Research Scholars and PG/UG students of Universities/ Engineering /Science/colleges.

#### **REGISTRATION FORM**

(Photocopy of this form is also accepted)
International Workshop
On

Recent /		Kleetronie
	ldvances in l	

Name: Dr. /Mr. /Ms			
Designation:			
Organization			
Address:			
Telephone/Mobile:			
E-mail:			
Details of Payment for Registration:			
DD	Ref.No		

Signature

Date: